CLAIMS

What is claimed is:

1   1.   A method of generating probabilities to calculate log-likelihood values in a

2   maximum *a posteriori* (MAP) processor for samples corresponding to data, the method

3   comprising the steps of:

4   (a) retrieving a first block of samples and a corresponding set of forward probabilities,

5   wherein the block of samples correspond to states of a merged trellis, wherein the merged trellis

6   provides combined probabilities of transition from one or more states at k-N, N an integer greater

7   than 1, to current states at time k as the set of forward probabilities; and

8   (b) updating the set of forward probabilities of the merged trellis for the current state at

9   time k, based on the block of samples and the corresponding set of forward probabilities.

1   2.   The invention as recited in claim 1, further comprising the steps of:

2   (c) retrieving a second block of samples and a corresponding set of backward

3   probabilities, wherein the samples correspond to states of the merged trellis, wherein the merged

4   trellis provides cumulative probabilities of transition from one or more states at time k+N to

5   current states at time k as the set of backward probabilities; and

6   (d) updating the set of backward probabilities of the merged trellis for the current state at

7   time k, based on the block of samples and the corresponding set of backward probabilities.

1   3.   The invention as recited in claim 2, further comprising the steps of computing log-

2   likelihood values from the updated forward and backward probabilities and generating a data

3   sequence for one or more blocks of samples corresponding to the log-likelihood values.

1   4.   The invention as recited in claim 1, further comprising the step of storing in or

2   reading from a memory each block of sample values for each update in step (a).

1   5.   The invention as recited in claim 1, wherein updating the forward probability for a

2   state in step (b) comprises the step of selecting the maximum combined probability for

3   transitions to the current state.

1   6.   The invention as recited in claim 5, wherein the step of selecting the maximum

2   combined probability for transitions to the current state k is given by:

3
$$A_j^k = \max{}^*_{i=0,1,\,\ldots,7}(A_i^{k-N} + \tilde{\Gamma}_{i,j}^k) \qquad \text{for } j = 0,1,\ldots, M\text{-}1,$$

4   where $\tilde{\Gamma}_{i,j}^k$ is the combined probability obtained by adding N individual branch matrices from

5   time k-N to time k in an original trellis, and M is the number of states.

1   7.   The invention as recited in claim 1, wherein the method is employed during a step

2   of either maximum *a posteriori* (MAP) detection or MAP decoding of received samples.

1   8.   The invention as recited in claim 1, wherein the method is implemented by a

2   processor in an integrated circuit.

1   9.   Apparatus generating probabilities to calculate log-likelihood values

2   corresponding to data, the apparatus comprising:

3   a maximum *a posteriori* (MAP) processor comprising:

4   a first module retrieving a first block of samples and a corresponding set of

5   forward probabilities, wherein the block of samples correspond to states of a merged

6   trellis, wherein the merged trellis provides combined probabilities of transition from one

7   or more states at k-N, N an integer greater than 1, to current states at time k as the set of

8   forward probabilities; and

9   a second module updating the set of forward probabilities of the merged trellis for

10   the current state at time k, based on the block of samples and the corresponding set of

11   forward probabilities.

1   10.   The invention as recited in claim 9, wherein the MAP processor further

2   comprises:

3   a third module retrieving a second block of samples and a corresponding set of

4   backward probabilities, wherein the samples correspond to states of the merged trellis,

5   wherein the merged trellis provides cumulative probabilities of transition from one or

6   more states at time k+N to current states at time k as the set of backward probabilities;

7   and

8   a fourth module updating the set of backward probabilities of the merged trellis

9 for the current state at time k, based on the block of samples and the corresponding set of

10 backward probabilities.

1 11. The invention as recited in claim 10, wherein the MAP processor is configured to

2 1) compute log-likelihood values from the updated forward and backward probabilities and 2)

3 generate a data sequence for one or more blocks of samples corresponding to the log-likelihood

4 values.

1 12. The invention as recited in claim 9, further comprising a memory, wherein the

2 MAP processor stores in or reads from the memory each block of sample values for each update.

1 13. The invention as recited in claim 9, wherein the second module updates the

2 forward probability for a state by selecting the maximum combined probability for transitions to

3 the current state..

1 14. The invention as recited in claim 13, wherein selecting the maximum combined

2 probability for transitions to the current state k is given by:

3
$$A_j^k = \max\nolimits^*_{i=0,1,\ldots,7}(A_i^{k-N} + \tilde{\Gamma}^k_{i,j}) \qquad \text{for } j = 0,1,\ldots,\text{M-1},$$

4 where $\tilde{\Gamma}^k_{i,j}$ is the combined probability obtained by adding N individual branch matrices from

5 time k-N to time k in an original trellis, and M is the number of states.

1 15. The invention as recited in claim 9, wherein the apparatus is a circuit embodied in

2 an integrated circuit.

1 16. A computer-readable medium having stored thereon a plurality of instructions, the

2 plurality of instructions including instructions which, when executed by a processor, cause the

3 processor to implement a method for generating log-likelihood values for data in a maximum *a*

4 *posteriori* (MAP) processor, the method comprising the steps of:

5 (a) retrieving a first block of samples and a corresponding set of forward probabilities,

6 wherein the block of samples correspond to states of a merged trellis, wherein the merged trellis

7 provides combined probabilities of transition from one or more states at k-N, N an integer greater

8 than 1, to current states at time k as the set of forward probabilities; and

9 (b) updating the set of forward probabilities of the merged trellis for the current state at

10   time k, based on the block of samples and the corresponding set of forward probabilities.

1   17.   The invention as recited in claim 16, further comprising the steps of:

2   (c) retrieving a second block of samples and a corresponding set of backward

3   probabilities, wherein the samples correspond to states of the merged trellis, wherein the merged

4   trellis provides cumulative probabilities of transition from one or more states at time $k+N$ to

5   current states at time k as the set of backward probabilities; and

6   (d) updating the set of backward probabilities of the merged trellis for the current state at

7   time k, based on the block of samples and the corresponding set of backward probabilities.